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FOR AP CLASS 11 EXAM PREPARATION

AP Class 11 2027 Model Paper · Electronics Engineering Technician

EXAM	YEAR	TYPE
AP Class 11	2027	Model Paper

SUBJECT

Electronics Engineering Technician

Model Question Paper

ELECTRONIC ENGINEERING TECHNICIAN

I YEAR

PAPER - I

CIRCUIT THEORY AND ELECTRONIC COMPONENTS

Time: 3 hours | Max. Marks: 50

Section - A

Note: 1. Answer all Questions **10 x 2 = 20**
2. Each Question Carries **TWO** Marks .

1. State Kirchhoffs Voltage Law.
2. What are the specifications of the Transformer?
3. Name the Different Types of Capacitor's according to Dielectric material used.
4. Define the Terms frequency, average Value.
5. Define Resonance.
6. Classify the switches according to Poles and Throws.
7. What is meant by MCB?
8. Write the classifications of PCB's.
9. What are the advantages of SMT over Pin Through Hole (PTH) molded?
10. What are the applications of Secondary cell?

Section - B

Note: 1. Answer any **FIVE** Question **5 x 6 = 30**
2. Each Question Carries **SIX** Marks

11. Calculate the equivalent resistance of resistors connected in series.
12. a) State faraday's Laws.
    b) what are the factors effects the Inductance value.
13. Find the Capacitance of Capacitor connected in parallel.
14. Derive an expression of RLC series resonance of an A.C Circuits.
15. Write the construction & Working ofElectromagnetic Relay.
16. Write the Procedure what steps taken for preparing PCB.
17. Give an idea of surface mount technology.
18. Write the Construction & Working of a Lead- Acid Cell with Neat Sketch .

Model Question Paper

ELECTRONIC ENGINEERING TECHNICIAN

I YEAR

PAPER - II

ELECTRONIC DEVICES & CIRCUITS

Time: 3 hours | Max. Marks: 50

Section - A

Note: 1. Answer all Questions
2. Each Question Carries **TWO** Marks .

10 x 2 = 20

1. Draw the Symbols of PNP& NPN Transistors.
2. Define Ripple factor of Full Wave Rectifier.
3. List the types of Biasing Circuits used in Amplifier.
4. Distinguish between Voltage Amplifier and Power Amplifier.
5. What are the advantages of Crystal Oscillator over other type.
6. What are the applications of PLL.
7. Draw the symbols of SCR.
8. List the application of all power Electronic Devices.
9. Draw the Power control schematic diagram.
10. List the application of Photo diode and Photo Transistor.

Section - B

Note: 1. Answer any **FIVE** Question
2. Each Question Carries **SIX** Marks

5 x 6 = 30

11. Describe the working of PN Junction Diode under forward Bias and Draw its VI-characteristics.
12. Draw & Describe the working of Centre Tapped Full wave rectifier with Wave forms.
13. Compare input & Output Characteristics of CB, CC and CE configurations.
14. Draw the circuit of Class- A Amplifier with Transformer load at the Collector and Explain the operation.
15. Draw & Explain working of R-C phase shift oscillator.
16. Explain the working of AstableMultivibrator using 555 I.C.
17. Draw and Explain the V-I Characteristics of Traic under forward bias.
18. Explain the construction & working Principle of LCD.

Model Question Paper

ELECTRONIC ENGINEERING TECHNICIAN

I YEAR

PAPER - III

DIGITAL ELECTRONICS & COMPUTER FUNDAMENTALS

Time: 3 hours **Max. Marks: 50**

Section - A

Note: 1. Answer all Questions **10 x 2 = 20**
2. Each Question Carries **TWO** Marks .

1. State De Morgans Laws.
2. List the different Digital logic families.
3. What are the applications of Multiplexer.
4. State and need of Register.
5. List the different types of semiconductor memories.
6. Define Cache memory.
7. Explain the concept of Router
8. List the different types of computer networks.
9. Define Op-code and operand.
10. Mention different types of instructions.

Section - B

Note: 1. Answer any **FIVE** Question **5 x 6 = 30**
2. Each Question Carries **SIX** Marks

11. Explain Binary, Octal,Hexa Decimal number systems and Compare with Decimal Systems.
12. Explain the working of Universal logic gates (NAND, NOR gates) using Truth Tables.
13. Draw the Full adder circuit and explain its operation with Truth Table.
14. Draw and Explain the working of 4 Bit –shift left register.
15. Explain D/A conversion using R-2R ladder Network.
16. Draw the block diagram of the computer and explain.
17. Explain different layers in TCP/IP protocol.
18. Draw and explain the block diagram of 8085 microprocessor.