Kerala Board

Answer Key

2024

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ANSWER KEY

CODE NO	FY453

FIRST YEAR HIGHER SECONDARY EXAMINATION MARCH 2024

PART III

SUBJECT : ELECTRONIC SYSTEMS

60 SCORES 2 HOURS

Qn. No	Sub Qns	Answer Key/Value Points	Score	Total Score
		(Answer any 5 questions from 1 to 6 - 1 score each)		
1		0.02S	1	1
2		Increases	1	1
3		JFET or MOSFET	1	1
4		Class C	1	1
5			1	1
6		NAND	1	1
		(Answer any 11 questions from 7 to 18 - 2 scores each)		
7	a b	Definition of time period Definition of frequency	1 1	2
8	a b	Red Red Red Gold Green Blue Brown Silver	1 1	2
9		Energy band diagrams of semiconductor and conductor	2 x 1	2
10		Any 2 comparisons between N type & P type semiconductors	2 x 1	2
11	a b	Need for voltage regulation 7812	1 1	2
12	a b	True Justification for fixing Q point at the centre of DC load line	1 1	2
13		Any 2 comparisons between voltage & power amplifiers	2 x 1	2

14		Symbol of SCR & LDR 1 Application of SCR & LDR	2 x ½ = 1 2 x ½ = 1	2
15		Circuit diagram of half adder Truth table	1 1	2
16		Any 2 uses of function generator	2 x 1	2
17		Block diagram of TDM	2	2
18	a b	Definition of FM Modulated waveform	1 1	2
		(Answer any 7 questions from 19 to 27 - 3 scores each)		
19	a b c	9μF 8 mH 23Ω	1 1 1	3
20	a b	0.7 V Circuit arrangement to obtain V-I Characteristics of a diode	1 2	3
21	a b c	Iron core inductor Trimmer Variable resistor	1 1 1	3
22	a b	Cut off Circuit diagram of transistor as a switch Output waveform	1 1 1	3
23		Any 2 comparison points of Zener & Avalanche breakdowns	2 x 1 ½	3
24		Need for biasing	3	3
25	a b	0 Circuit diagram of astable multivibrator & output waveform	1 2	3
26		Diagram to convert galvanometer into voltmeter Explanation	2 1	3
27		K – map representation Grouping Equation $y = \bar{A}\,\bar{B} + C\bar{D}$	1 1 1	3

		(Answer any 3 questions from 28 to 32 – 4 scores each)		
28	a b	Statement of KVL I = 2 A	2 2	4
29	a b	81.2 % Diagram of centre tapped rectifier Working	1 2 1	4
30	a b	Voltage Working of N channel JFET	1 3	4
31	a b	Frequency response of RC coupled amplifier DC load line Q point marking	2 1 1	4
32	a b	Statement of De Morgan's Theorems Ex -OR using NAND	2 x 1 = 2 2	4

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